| | T | | r | |
|-------------|---------|--|--------------------|------------------|
| L Number | Hits | Search Text | DB | Time stamp |
| 1 | 105425 | defective | USPAT; | 2004/08/03 |
| _ | | | US-PGPUB | 07:47 |
| 2 | 201330 | skip\$ bypass\$ | USPAT; | 2004/08/03 |
| | | l , , , , , , , , , , , , , , , , , , , | US-PGPUB | 07:48 |
| 3 | 1278 | defective with (skip\$ bypass\$) | USPAT; | 2004/08/03 |
| ۱, | 2170505 | | US-PGPUB | 07:48 |
| 4 | 3170525 | processor unit device circuit controller | USPAT; | 2004/08/03 |
| 5 | 656 | (defective with (skip\$ bypass\$)) same | US-PGPUB | 07:49 2004/08/03 |
| 3 | 656 | (processor unit device circuit | USPAT; US-PGPUB | 07:50 |
| | | controller) | US-PGPUB | 07.50 |
| 6 | 433 | (defective with (skip\$ bypass\$)) with | USPAT; | 2004/08/03 |
| ~ | 155 | (processor unit device circuit | US-PGPUB | 07:50 |
| | | controller) | OB TOTOB | 07.30 |
| 8 | 29 | graphic and ((defective with (skip\$ | USPAT; | 2004/08/03 |
| _ | | bypass\$)) same (processor unit device | US-PGPUB | 07:51 |
| | | circuit controller)) | | ***** |
| 7 | 7 | graphic and ((defective with (skip\$ | USPAT; | 2004/08/03 |
| | | bypass\$)) with (processor unit device | US-PGPUB | 09:06 |
| | | circuit controller)) | | |
| 9 | 22 | (graphic and ((defective with (skip\$ | USPAT; | 2004/08/03 |
| | | bypass\$)) same (processor unit device | US-PGPUB | 08:03 |
| | | circuit controller))) not (graphic and | | |
| | | ((defective with (skip\$ bypass\$)) with | | |
| | | (processor unit device circuit | | |
| | | controller))) | | |
| 10 | 426 | | USPAT; | 2004/08/03 |
| | | (processor unit device circuit | US-PGPUB | 09:04 |
| | | controller)) not (graphic and ((defective | | |
| | | <pre>with (skip\$ bypass\$)) same (processor unit device circuit controller)))</pre> | | |
| 11 | 119141 | dnit device circuit controller;;; | IICDAM. | 2004/00/02 |
| 1 1 | 119141 | Laulty | USPAT; US-PGPUB | 2004/08/03 |
| 12 | 1439 | (skip\$ bypass\$) with fault\$ | USPAT; | 2004/08/03 |
| 12 | 1135 | \barpy bypassy, with ladity | US-PGPUB | 09:04 |
| 13 | 851 | (processor unit device circuit | USPAT; | 2004/08/03 |
| | | controller) same ((skip\$ bypass\$) with | US-PGPUB | 09:05 |
| | | fault\$) | OBTOLOB | 05.03 |
| 14 | 571 | · · · | USPAT; | 2004/08/03 |
| | | controller) with ((skip\$ bypass\$) with | US-PGPUB | 09:06 |
| | | fault\$) | | |
| 15 | 34 | | USPAT; | 2004/08/03 |
| | | circuit controller) same ((skip\$ bypass\$) | US-PGPUB | 09:06 |
| | | with fault\$)) | |] [|
| 16 | 19 | | USPAT; | 2004/08/03 |
| | | circuit controller) with ((skip\$ bypass\$) | US-PGPUB | 09:06 |
| | | <pre>with fault\$))</pre> | | |